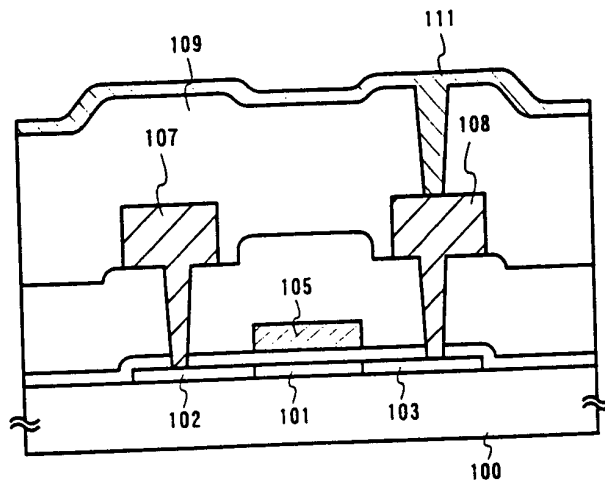
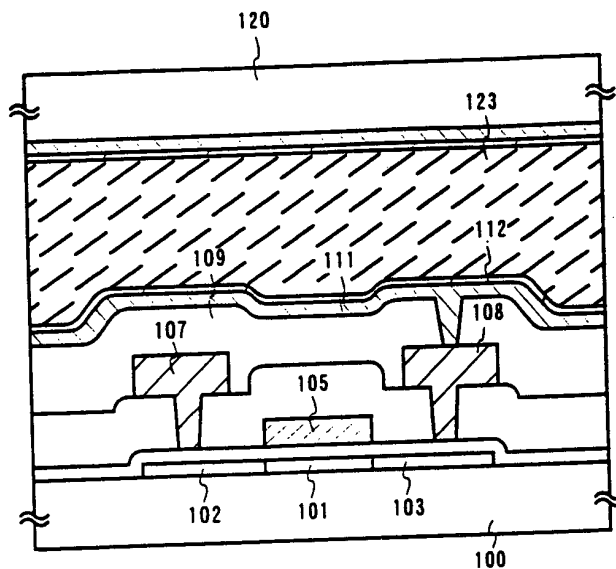


Fig.2

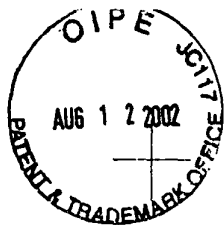


PRIOR ART

Fig.3



PRIOR ART



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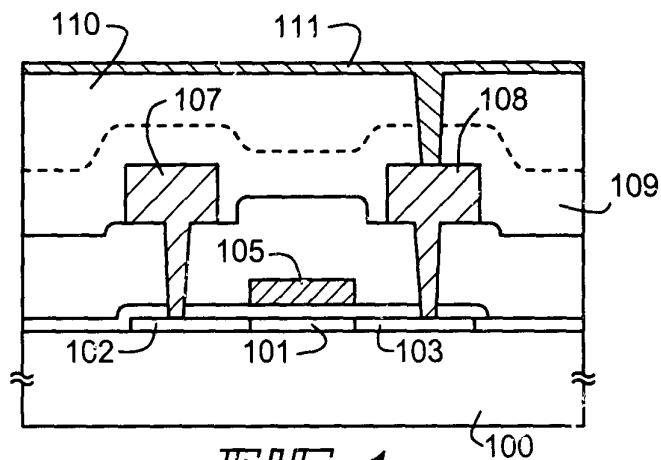


FIG. 1

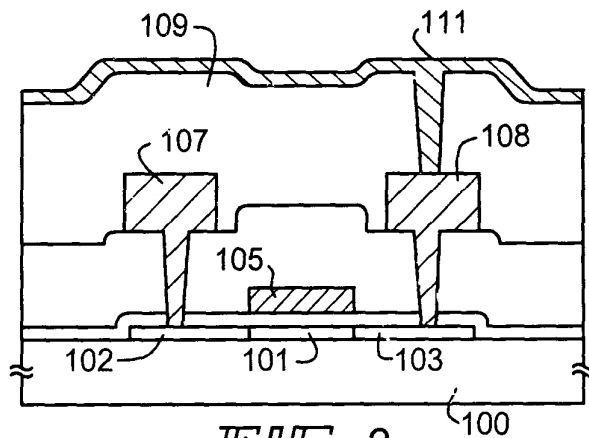


FIG. 2
PRIOR ART

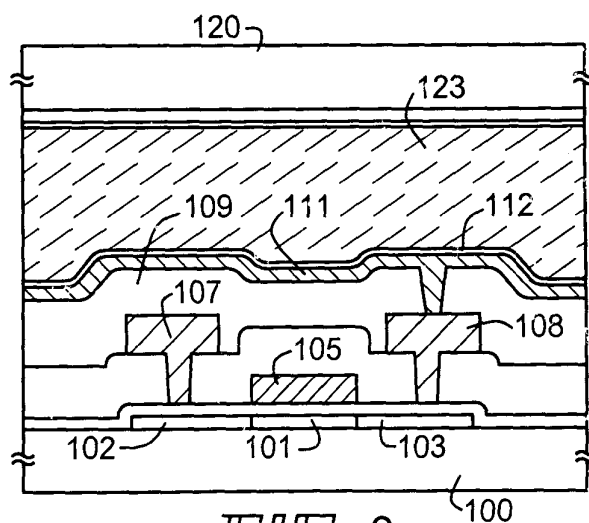


FIG. 3
PRIOR ART



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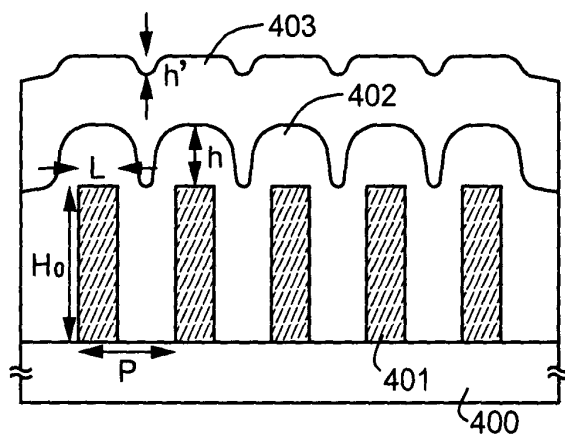


FIG. 4

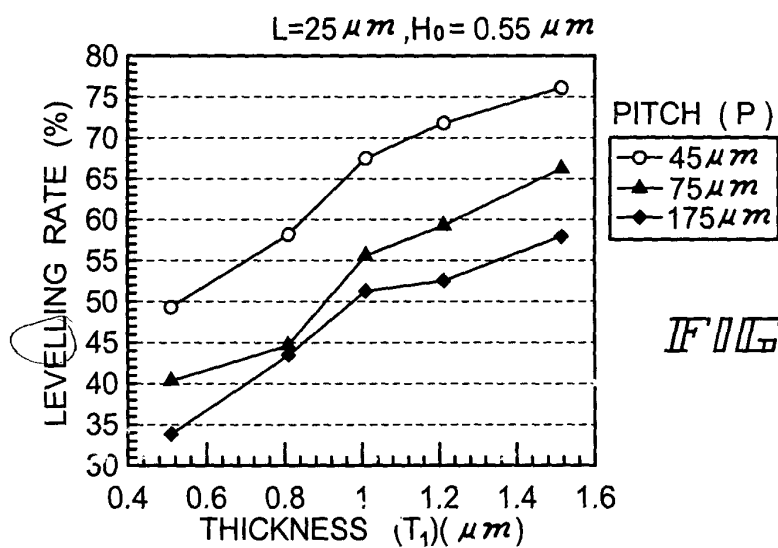


FIG. 5

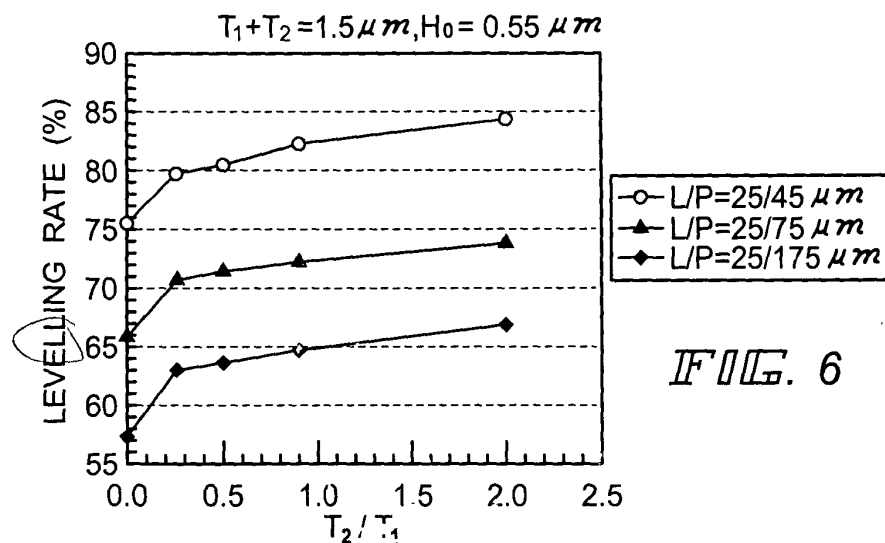


FIG. 6



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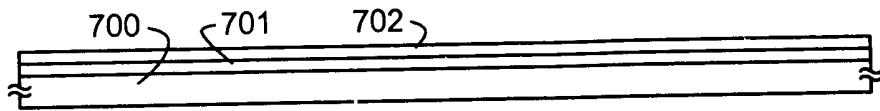


FIG. 7A

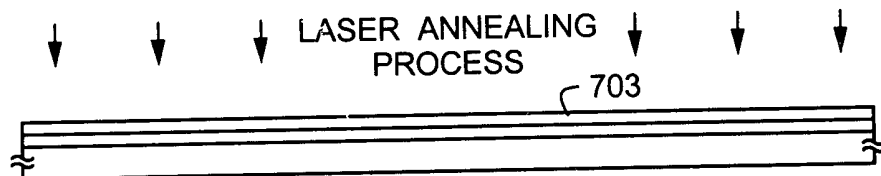


FIG. 7B

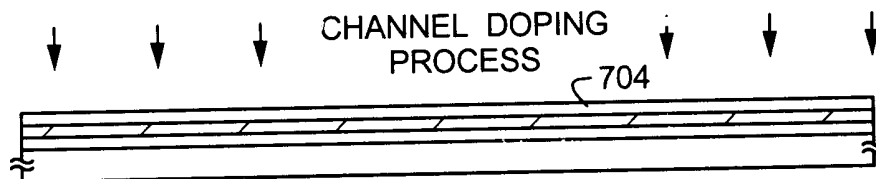


FIG. 7C

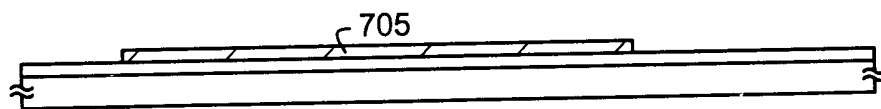


FIG. 7D

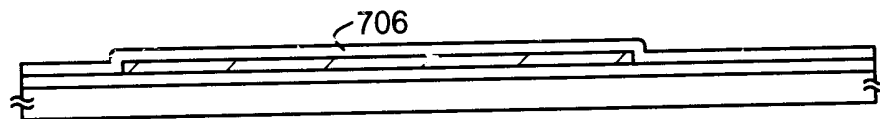


FIG. 7E

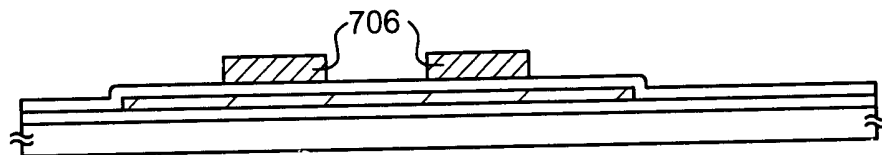


FIG. 7F

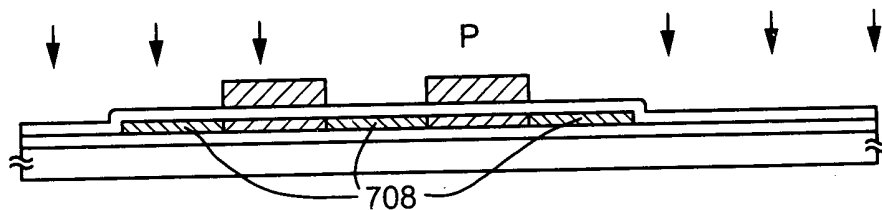


FIG. 7G





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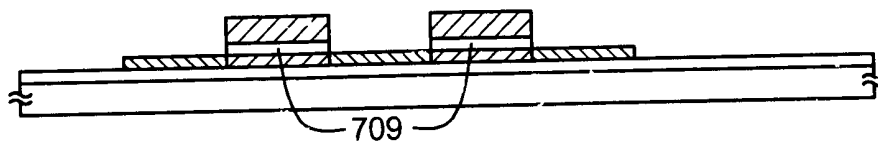


FIG. 8A

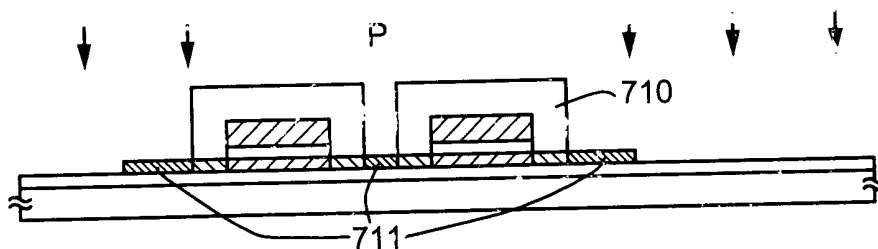


FIG. 8B

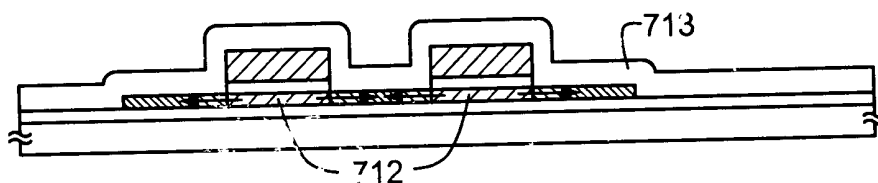


FIG. 8C

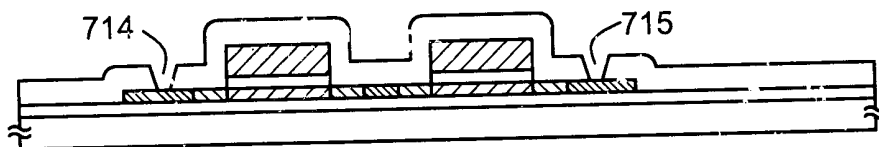


FIG. 8D

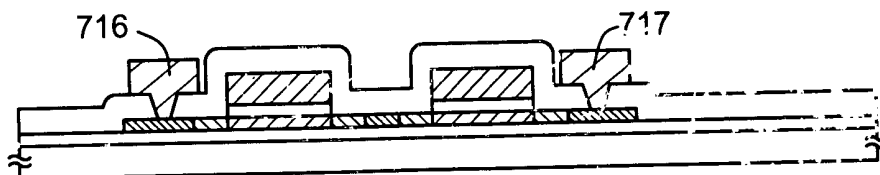


FIG. 8E





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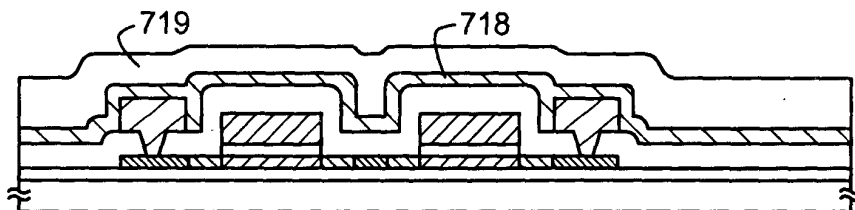


FIG. 9A

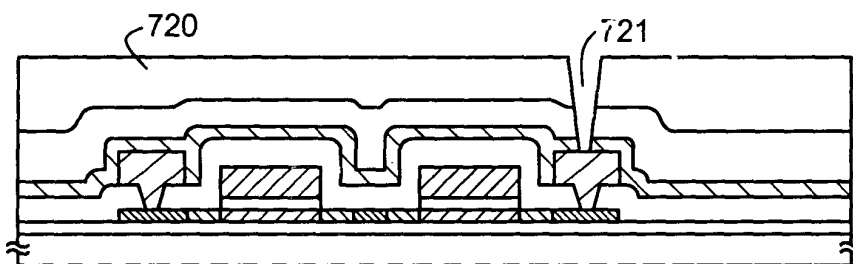
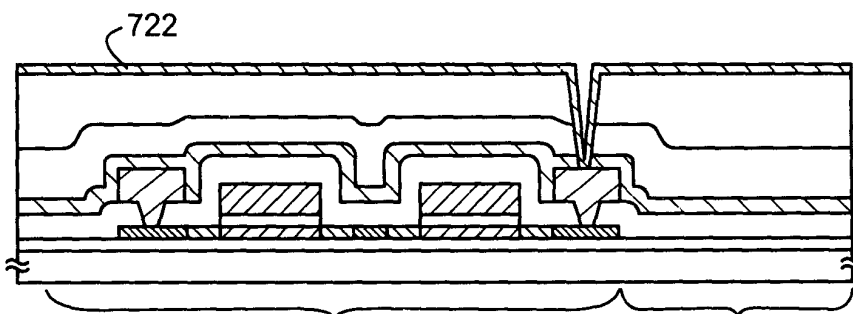


FIG. 9B



PIXEL TFT REGION

DISPLAY REGION

FIG. 9C





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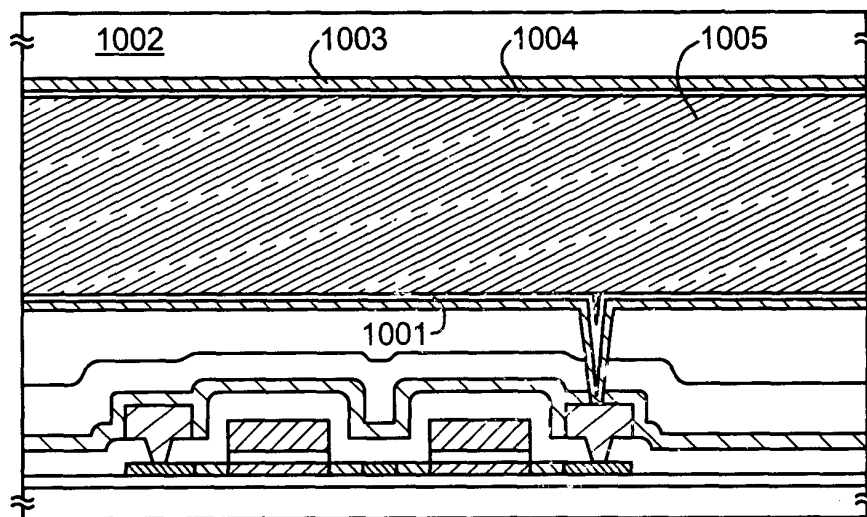


FIG. 10

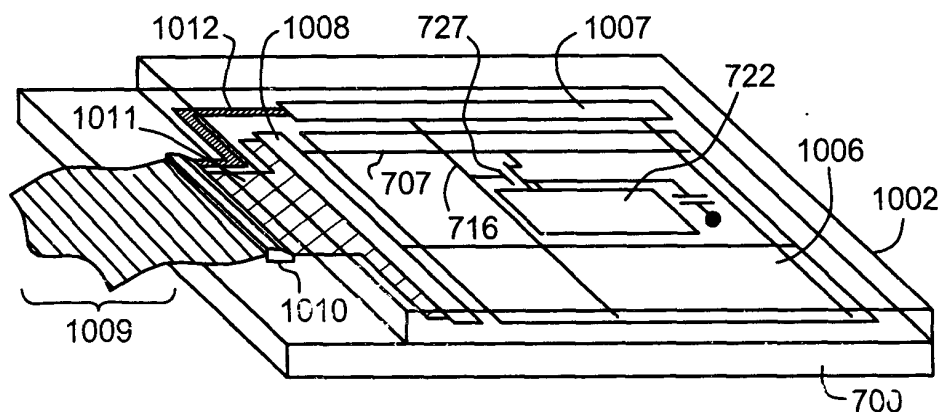


FIG. 11





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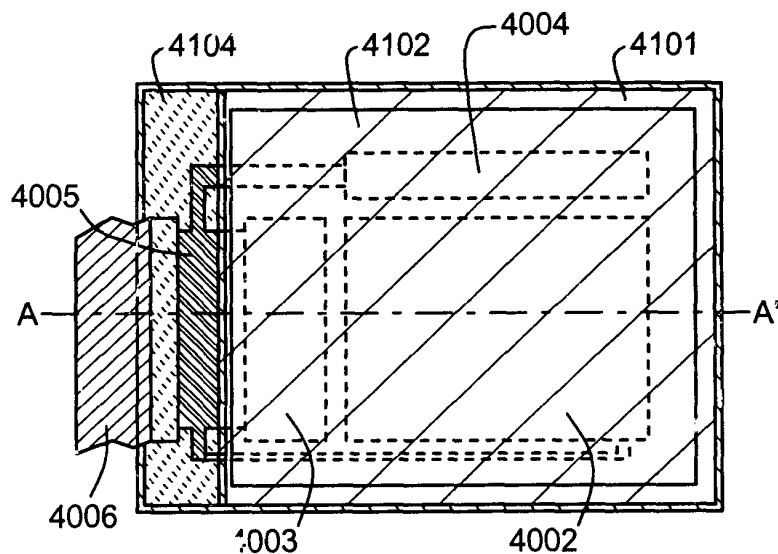


FIG. 12

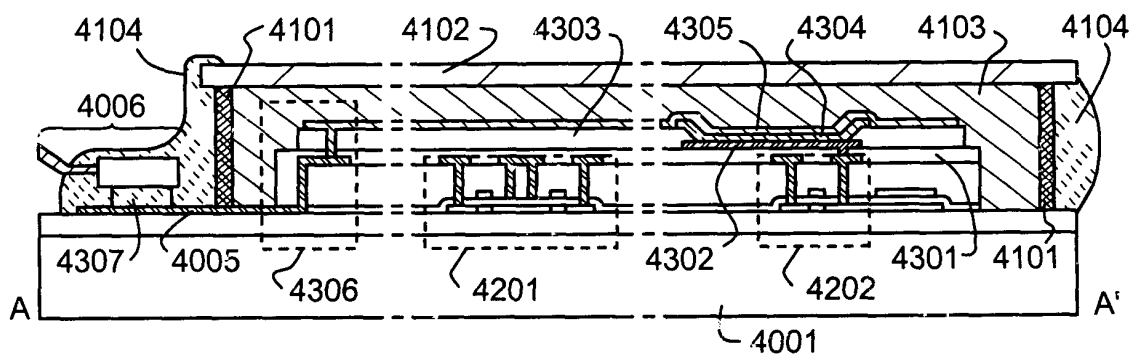


FIG. 13



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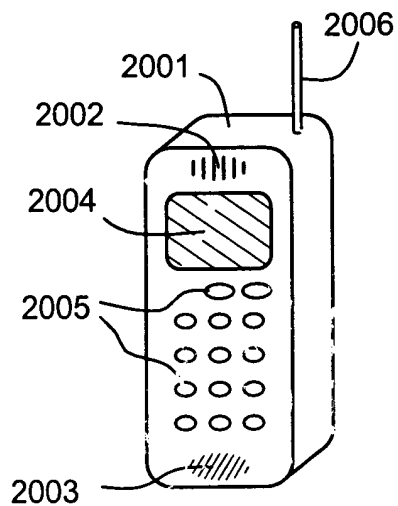


FIG. 14A

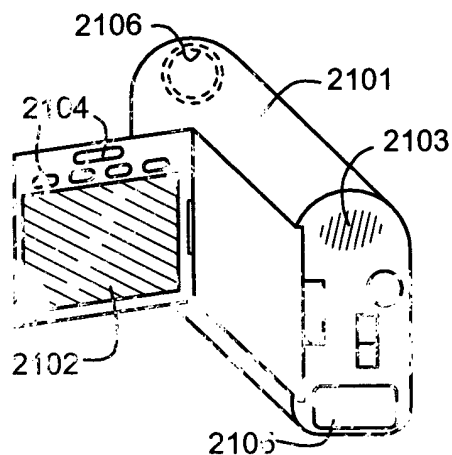


FIG. 14B

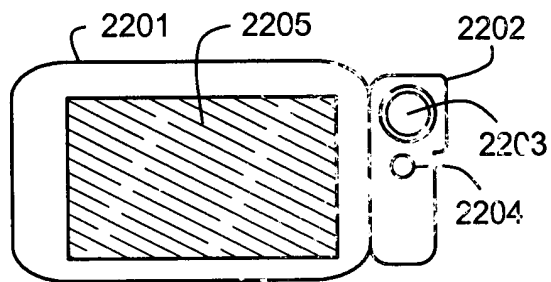


FIG. 14C

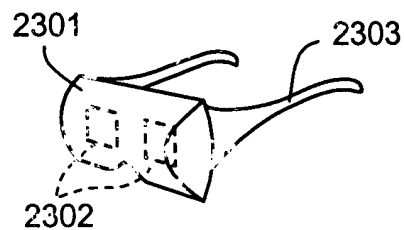


FIG. 14D

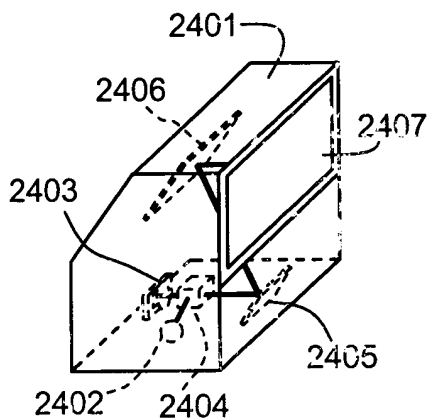


FIG. 14E

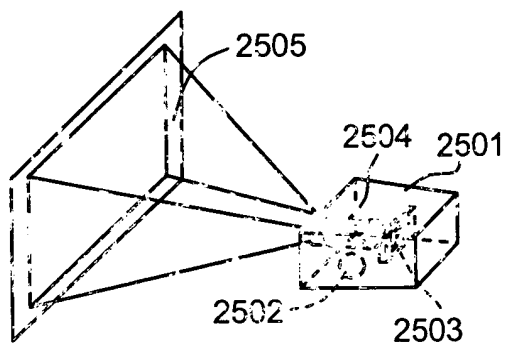


FIG. 14F